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Date: August 18, 2008

To:	U.S. Patent Office	From:	Scott Charles Richardson
Fax:	(571) 273-8300	Fax:	(571)281-2826
Phone:		Pages:	4 (including fax cover)
Your Ref.:	U.S. serial no. 10/646,282	Our Ref.:	GTW-0164(P1960)
Re:	Telephone Interview Summary		

Comments:

On the accompanying pages please find the following for the patent application identified above:

- 1) Telephone Interview Summary

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CENTRAL FAX CENTER****AUG 18 2008**Attorney Docket No.: GTW-0164**PATENT****IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): Frank LIEBENOW	Conf. No.: 9378
Application No.: 10/646,282	Art Unit: 2193
Filed: August 21, 2003	Examiner: William H. WOOD
Title: HUFFMAN-L COMPILER OPTIMIZED FOR CELL-BASED COMPUTERS OR OTHER COMPUTERS HAVING RECONFIGURABLE INSTRUCTION SETS	

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INTERVIEW SUMMARY

Sir:

The applicant appreciates the courtesies extended to applicant's undersigned representative during the telephone interview of August 12, 2008. During the telephone interview applicant's representative inquired whether this Amendment would be entered in the application as an After-Final Amendment, and discussed some of the distinguishing features of the claims.

During the Interview applications representative discussed the difference between static instruction count and dynamic instruction count, as these two parameters are used in optimization of a code sequence. For example, the static instruction count can be determined by scanning the

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code and counting the number of times a given instruction appears in the code. The dynamic instruction count can be determined by taking into account the number of times the program loops back to perform a particular instruction multiple times for a give code sequence. This may be determined, for example, through the use of loop analysis.

In regards to the present claimed invention, the claims have been amended to encompass both concepts, that is, both static instruction count and dynamic instruction count for tuning an instruction set. For example, Claim 1 is amended by this paper to recite "scanning the code sequence to determine a static frequency of operations in the code sequence [and] performing a loop analysis to determine an executed frequency of operations for the code sequence"—language intended to more clearly define the metes and bounds of the claimed invention to cover both static instruction count and dynamic instruction count. Support for these amendment can be found throughout the disclosure, for example, paragraphs [0041] through [0043] disclose scanning the code to determine a histogram of operations (static information), while paragraphs [0051] and [0059] disclose use of the executed frequency of operations (dynamic information) by further performing loop analysis to modify the static information to account for loops that may get executed multiple times.

It is believed that the claims now pending following entry of the August 18, 2008 Amendment are distinguished from the art cited in the pending rejection since such conventional systems do not use both static instruction count and dynamic instruction count for tuning an instruction set.

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CONCLUSION

In view of the foregoing, it is respectfully submitted that the application is in condition for allowance. However, in the event there are any unresolved issues, the Examiner is kindly invited to contact applicant's representative, Scott Richardson, by telephone at (571)970-6835 so that such issues may be resolved as expeditiously as possible.

Respectfully submitted,



Scott Charles Richardson
Reg. No. 43,436

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Date: August 18, 2008